

General Description

Maxim's redesigned DG444/DG445 analog switches now feature on-resistance matching (4 Ω max) between switches and guaranteed on-resistance flatness over the signal range (9 Ω max). These low on-resistance switches conduct equally well in either direction. They guarantee low charge injection (10pC max), low power consumption (35µW max), and an ESD tolerance of 2000V minimum per Method 3015.7. The new design offers lower off leakage current over temperature (less than 5nA at +85°C).

The DG444/DG445 are quad, single-pole/single-throw (SPST) analog switches. The DG444 has 4 normally closed switches and the DG445 has 4 normally open switches. Switching times are less than 250ns for ton and less than 70ns for toff. Operation is from a single +10V to +30V supply, or bipolar ±4.5V to ±20V supplies. Maxim's improved DG444/DG445 continue to be fabricated with a 44V silicon-gate process.

Applications

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Sample-and-Hold Circuits	Communication Systems
Test Equipment	Battery-Operated Systems
Heads-Up Displays	PBX, PABX
Guidance and Control Systems	Audio Signal Routing
Military Radios	Modems/Faxes

New Features

- ♦ Plug-In Upgrades for Industry-Standard DG444/DG445
- ♦ Improved ron Match Between Channels (4Ω max)
- ♦ Guaranteed rFLAT(ON) Over Signal Range (9Ω max)
- **♦** Improved Charge Injection (10pC max)
- **♦** Improved Off Leakage Current Over Temperature (<5nA at +85°C)
- ♦ Withstand Electrostatic Discharge (2000V min) per Method 3015.7

Existing Features

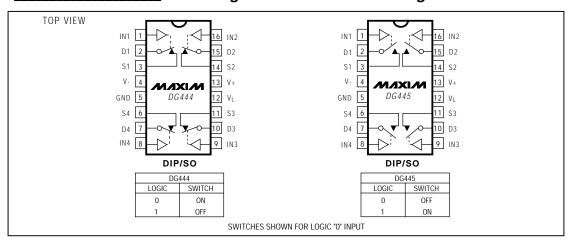
- ♦ Low rds(ON) (85 Ω max)
- Single-Supply Operation +10V to +30V Bipolar-Supply Operation ±4.5V to ±20V
- **♦** Low Power Consumption (35µW max)
- ♦ Rail-to-Rail Signal Handling
- ♦ TTL/CMOS-Logic Compatible

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
DG444CJ	0°C to +70°C	16 Plastic DIP
DG444DY	0°C to +70°C	16 Narrow SO
DG444C/D	0°C to +70°C	Dice*
DG444DJ	-40°C to +85°C	16 Plastic DIP
DG444DY	-40°C to +85°C	16 Narrow SO

Ordering Information continued at end of data sheet. Contact factory for dice specifications

Pin Configurations/Functional Diagrams/Truth Tables



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ABSOLUTE MAXIMUM RATINGS

Voltage Referenced to V-	Continuous Power Dissipation (T _A = +70°C)
V+44V	16-Pin Plastic DIP (derate 10.53mW/°C above +70°C) 842mW
GND25V	16-Pin Narrow SO (derate 8.70mW/°C above +70°C)696mW
V _L (GND -0.3V) to (V+ +0.3V)	Operating Temperature Ranges
Digital Inputs V _S , V _D (Note 1)(V2V) to (V+ +2V) or 30mA	DG444C/DG445C0°C to +70°C
(whichever occurs first)	DG444D/DG445D40°C to +85°C
Continuous Current (any terminal)30mA	Storage Temperature Range65°C to +150°C
Peak Current, S or D (pulsed at 1ms, 10% duty cycle max) .100mA	Lead Temperature (soldering, 10sec)+300°C

Note 1: Signals on S, D, or IN exceeding V+ or V- are clamped by internal diodes. Limit forward current to maximum current rating.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS—Dual Supplies

 $(V+=15V, V-=-15V, V_L=5V, GND=0V, V_{INH}=2.4V, V_{INL}=0.8\dot{V}, \dot{T}_A=T_{MIN}$ to T_{MAX} , unless otherwise noted.)

PARAMETER	SYMBOL	CONDIT	IONS	MIN	TYP (Note 2)	MAX	UNITS
SWITCH							•
Analog Signal Range	V _{ANALOG}	(Note 3)		-15		15	V
Drain-Source	rds(on)	V+ = 13.5V, V- = -13.5V, $VD = \pm 8.5V.$	T _A = +25°C		50	85	Ω
On-Resistance	TDS(ON)	$I_S = -10 \text{mA}$	TA = TMIN to TMAX			100	
On-Resistance Match	Arnovous	V _D = ±10V,	T _A = +25°C			4	0
Between Channels (Note 4)	Δr _{DS} (ON)	Is = -10mA	TA = TMIN to TMAX			5	Ω
On-Resistance Flatness (Note 4)	FEL ATIONS	$V_D = \pm 5V$, $I_S = -10$ mA	T _A = +25°C			9	Ω
On-Resistance Flathess (Note 4)	rflat(ON)		$T_A = T_{MIN}$ to T_{MAX}			15	1 22
Source Leakage Current	IS(OFF)	V + = 16.5V, V - = -16.5V, $V_D = \pm 15.5V,$ $V_S = \mp 15.5V$	T _A = +25°C	-0.50	0.01	0.50	nA
(Note 5)			TA = TMIN to TMAX	-5		5	
Drain-Off Leakage Current	ID(OFF)	V+ = 16.5V, V- = -16.5V, $V_D = \pm 15.5V,$ $V_S = \mp 15.5V$	T _A = +25°C	-0.50	0.01	0.50	nA
(Note 5)			TA = TMIN to TMAX	-5		5	IIA
Drain-On Leakage Current	I _{D(ON)}	V+ = 16.5V, V- = -16.5V,	T _A = +25°C	-0.50	0.08	0.50	nA
(Note 5)	or Is(ON)	$V_D = \pm 15.5V$, $V_S = \pm 15.5V$	$T_A = T_{MIN}$ to T_{MAX}	-10		10	IIA
INPUT			•				
Input Current with Input Voltage High	linh	V _{IN} = 2.4V, all others = 0).8V	-0.5	-0.00001	0.5	μΑ
Input Current with Input Voltage Low	linl	V _{IN} = 0.8V, all others = 2	2.4V	-0.5	-0.00001	0.5	μA

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP (Note 2)	MAX	UNITS
SUPPLY							
Power-Supply Range	V+, V-			±4.5		±20.0	V
Positive Supply Current	1+	All channels on or off, V+ = 16.5V, V- = -16.5V, V _{IN} = 0V or 5V	T _A = +25°C	-1	0.001	1	
Positive Supply Current	1+		TA = TMIN to TMAX	-5		5	- μA
Nagativa Supply Current	-	All channels on or off,	T _A = +25°C	-1	-0.0001	1	
Negative Supply Current	-	V+ = 16.5V, V- = -16.5V, V _{IN} = 0V or 5V	$T_A = T_{MIN}$ to T_{MAX}	-5		5	μΑ
Logic Supply Current	I.	All channels on or off,	T _A = +25°C	-1	0.001	1	
Logic Supply Current	IL	V + = 16.5V, V - = -16.5V, $V_{IN} = 0V \text{ or } 5V$	TA = TMIN to TMAX	-5		5	μΑ
Ground Current		All channels on or off, V+ = 16.5V, V- = -16.5V, V _{IN} = 0V or 5V	T _A = +25°C	-1	-0.0001	1	μА
Ground Current	IGND		TA = TMIN to TMAX	-5		5	
DYNAMIC					-		
Turn-On Time	ton	$V_S = \pm 10V$, Figure 2	T _A = +25°C		150	250	ns
Turn-Off Time	toff	DG444, V _S = ±10V, Figure 2	T _A = +25°C		90	120	ns
Turn-On Time	IOFF	DG445, V _S = ±10V, Figure 2	T _A = +25°C		110	170	ns
Charge Injection (Note 3)	Q	$C_L = 1nF$, $V_{GEN} = 0V$, $R_{GEN} = 0\Omega$, Figure 3	T _A = +25°C		5	10	рС
Off-Isolation Rejection Ratio (Note 6)	OIRR	$R_L = 50\Omega$, $C_L = 5pF$, $f = 1MHz$, Figure 4	T _A = +25°C		60		dB
Crosstalk (Note 7)		$R_L - 50\Omega$, $C_L = 5pF$, $f = 1MHz$, Figure 5	T _A = +25°C		100		dB
Source-Off Capacitance	Cs(off)	f = 1MHz, Figure 6	T _A = +25°C		4		pF
Drain-Off Capacitance	CD(OFF)	$f = 1MHz$, Figure 6 $T_A = +25$ °C			4		pF
Source-On Capacitance	Cs(on)	$f = 1MHz$, Figure 7 $T_A = +25$ °C			16		pF
Drain-On Capacitance	C _D (ON)	f = 1MHz, Figure 7	T _A = +25°C		16		pF

ELECTRICAL CHARACTERISTICS—Single Supply

 $(V + = 12V, V - = 0V, V_L = 5V, GND = 0V, V_{INH} = 2.4V, V_{INL} = 0.8V, T_A = T_{MIN}$ to T_{MAX} , unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP (Note 2)	MAX	UNITS
SWITCH							l
Analog Signal Range	V _{ANALOG}	(Note 3)		0		12	V
Drain-Source	rds(on)	V+ = 10.8V, V _L = 5.25V,	T _A = +25°C		100	160	Ω
On-Resistance	103(011)	$V_D = 3V, 8V,$ $I_S = -10mA$	TA = TMIN to TMAX			200	- 12
SUPPLY	•			•			
Power-Supply Range	V+, V-			10.8		24.0	V
Power-Supply Current	1+	All channels on or off,	$T_A = +25^{\circ}C$	-1	0.001	1	μА
rower-supply current	1+	$V_{IN} = 0V \text{ or } 5V$	TA = TMIN to TMAX	-5		5	
Negative Supply Current	I-	All channels on or off,	T _A = +25°C	-1	-0.0001	1	μА
negative Supply Current	1-	$V_{IN} = 0V \text{ or } 5V$	$T_A = T_{MIN}$ to T_{MAX}	-5		5	μΑ
Logic Supply Current	lı .	All channels on or off,	T _A = +25°C	-1	0.001	1	μА
Logic Supply Current	'L	$V_{IN} = 0V \text{ or } 5V$	TA = TMIN to TMAX	-5		5	
Ground Current	lovo	All channels on or off,	T _A = +25°C	-1	-0.0001	1	
Ground Current	IGND	$V_{IN} = 0V \text{ or } 5V$	$T_A = T_{MIN}$ to T_{MAX}	-5		5	μΑ
DYNAMIC	•						
Turn-On Time	ton	$V_S = 8V$, Figure 2 $T_A = +25$ °C			300	400	ns
Turn-Off Time	toff	$V_S = 8V$, Figure 2 $T_A = +25$ °C			60	200	ns
Charge Injection (Note 3)	Q	$C_L = 1nF$, $V_{GEN} = 0V$, $R_{GEN} = 0\Omega$, Figure 3 $T_A = +25^{\circ}C$			5	10	рС

Note 2: Typical values are for design aid only, are not guaranteed and are not subject to production testing. The algebraic convention, where the most negative value is a minimum and the most positive value a maximum, is used in this data sheet. Note 3: Guaranteed by design.

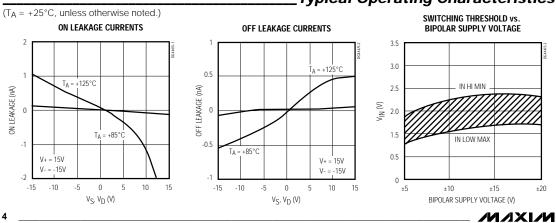
Note 4: On-resistance match between channels and flatness are guaranteed only with bipolar-supply operation. Flatness is defined as the difference between the maximum and the minimum value of on-resistance as measured at the extremes of the speci-

Note 5: Leakage parameters Is(OFF), ID(OFF), ID(ON), and IS(ON), are100% tested at the maximum rated hot temperature and guaranteed at +25°C.

Note 6: Off-Isolation Rejection Ratio = 20log (V_D/V_S), V_D = output, V_S = input to off switch.

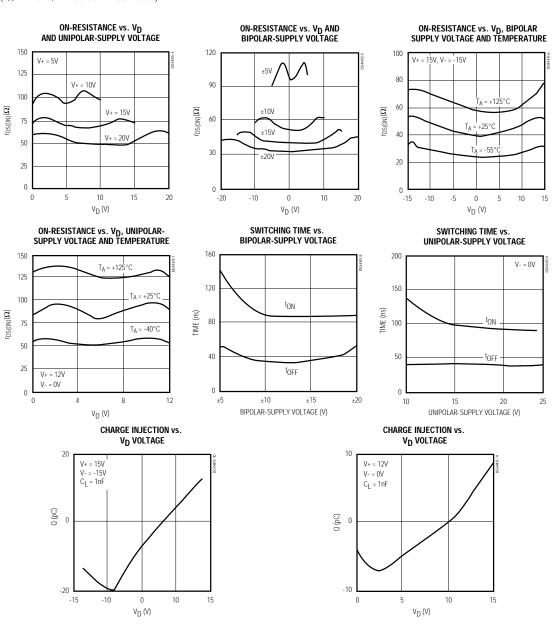
Note 7: Between any two switches.

Typical Operating Characteristics



Typical Operating Characteristics (continued)

 $(T_A = +25^{\circ}C, unless otherwise noted.)$



Pin Description

PIN	NAME	FUNCTION
1, 16, 9, 8	IN1-IN4	Logic Control Inputs
2, 15, 10, 7	D1-D4	Drain Outputs
3, 14, 11, 6	S1-S4	Source Outputs
4	V-	Negative Supply-Voltage Input
5	GND	Ground
12	VL	Logic Supply-Voltage Input
13 V+		Positive Supply-Voltage Input— connected to substrate

_Applications Information General Operation

- 1. Switches are open when power is off.
- 2. IN, D, and S should not exceed V+ or V-, even with the power off.
- 3. Switch leakage is from each analog switch terminal to V+ or V-, not to other switch terminals.

Operation with Supply Voltages Other Than ±15V

Using supply voltages other than $\pm 15V$ will reduce the analog signal range. The DG444/DG445 switches operate with $\pm 4.5V$ to $\pm 20V$ bipolar supplies or with a +10V to +30V single supply: connect V- to 0V when operating with a single supply. Also, all device types can operate with unbalanced supplies such as +24V and -5V. V_L must be connected to +5V to be TTL compatible, or to V+ for CMOS-logic level inputs. The *Typical Operating*

Characteristics graphs show typical on-resistance with $\pm 20V$, $\pm 15V$, $\pm 10V$, and $\pm 5V$ supplies. (Switching times increase by a factor of two or more for operation at $\pm 5V$.)

Overvoltage Protection

Proper power-supply sequencing is recommended for all CMOS devices. Do not exceed the absolute maximum ratings because stresses beyond the listed ratings may cause permanent damage to the devices. Always sequence V+ on first, followed by V_L , V-, and logic inputs. If power-supply sequencing is not possible, add two small, external signal diodes in series with supply pins for overvoltage protection (Figure 1). Adding diodes reduces the analog signal range to 1V below V+ and 1V above V-, but low switch resistance and low leakage characteristics are unaffected. Device operation is unchanged, and the difference between V+ and V-should not exceed +44V.

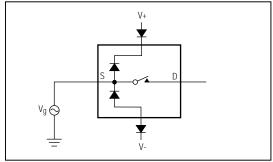


Figure 1. Overvoltage Protection Using External Blocking Diodes

Timing Diagrams/Test Circuits

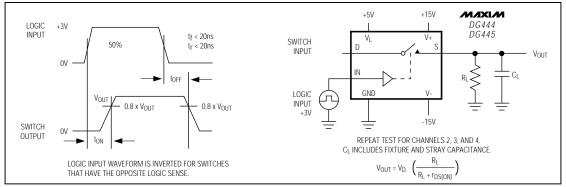


Figure 2. Switching Time

Timing Diagrams/Test Circuits (continued)

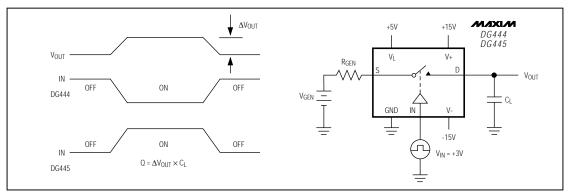


Figure 3. Charge Injection

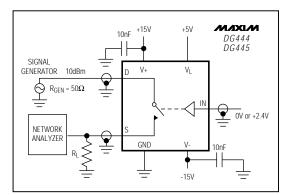


Figure 4. Off-Isolation Rejection Ratio

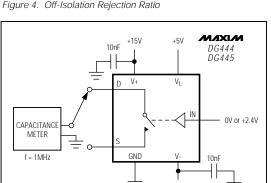


Figure 6. Source/Drain-Off Capacitance

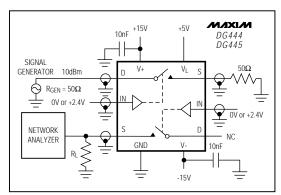


Figure 5. Crosstalk

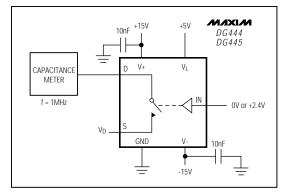


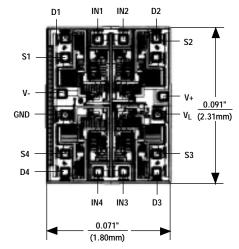
Figure 7. Source/Drain-On Capacitance

_Ordering Information (continued)

PART	TEMP. RANGE	PIN-PACKAGE
DG445CJ	0°C to +70°C	16 Plastic DIP
DG445CY	0°C to +70°C	16 Narrow SO
DG445C/D	0°C to +70°C	Dice*
DG445DJ	-40°C to +85°C	16 Plastic DIP
DG445DY	-40°C to +85°C	16 Narrow SO

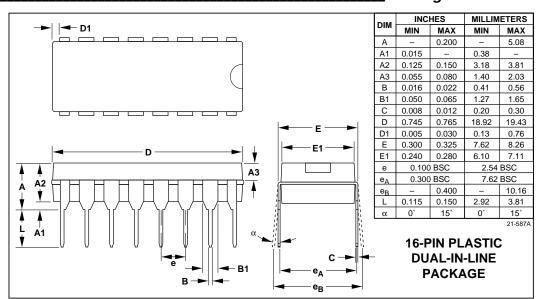
Contact factory for dice specifications.

__Chip Topography



TRANSISTOR COUNT: 126 SUBSTRATE CONNECTED TO V+

Package Information



Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.

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