National Semiconductor

LM109/LM309 5-Volt Regulator

General Description

The LM109 series are complete 5V regulators fabricated on a single silicon chip. They are designed for local regulation on digital logic cards, eliminating the distribution problems association with single-point regulation. The devices are available in two standard transistor packages. In the solidkovar TO-5 header, it can deliver output currents in excess of 200 mA, if adequate heat sinking is provided. With the TO-3 power package, the available output current is greater than 1A.

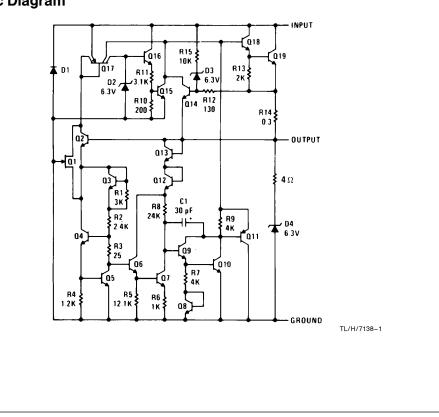
The regulators are essentially blowout proof. Current limiting is included to limit the peak output current to a safe value. In addition, thermal shutdown is provided to keep the IC from overheating. If internal dissipation becomes too great, the regulator will shut down to prevent excessive heating.

Considerable effort was expended to make these devices easy to use and to minimize the number of external components. It is not necessary to bypass the output, although this does improve transient response somewhat. Input bypassing is needed, however, if the regulator is located very far from the filter capacitor of the power supply. Stability is also achieved by methods that provide very good rejection of load or line transients as are usually seen with TTL logic. Although designed primarily as a fixed-voltage regulator, the output of the LM109 series can be set to voltages above 5V, as shown. It is also possible to use the circuits as the control element in precision regulators, taking advantage of the good current-handling capability and the thermal overload protection.

Features

- Specified to be compatible, worst case, with TTL and DTL
- Output current in excess of 1A
- Internal thermal overload protection
- No external components required

Schematic Diagram



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Absolute Maximur		Operating Junction Temperature Range					
please contact the National Semiconductor Sales Office/Distributors for availability and specifications.		LM109	−55°C to +150°C 0°C to +125°C				
		LM309					
(Note 3)		Storage Temperature Range	-65°C to +150°C				
Input Voltage	35V	Lead Temperature (Soldering, 10 sec.	300°C				
Power Dissipation	Internally Limited	Lead remperature (boldening, ro see.	.) 000 0				

Electrical Characteristics (Note 1)

Parameter	Conditions	LM109			LM309			Units
		Min	Тур	Мах	Min	Тур	Max	
Output Voltage	$T_j = 25^{\circ}C$	4.7	5.05	5.3	4.8	5.05	5.2	V
Line Regulation	$\begin{array}{l} T_{j}=25^{\circ}C\\ 7.10V\leqV_{IN}\leq25V \end{array}$		4.0	50		4.0	50	mV
Load Regulation TO-39 Package TO-3 Package	$\begin{array}{l} T_{j} = 25^{\circ}C \\ 5 \ mA \leq I_{OUT} \leq 0.5A \\ 5 \ mA \leq I_{OUT} \leq 1.5A \end{array}$		15 15	50 100		15 15	50 100	mV mV
Output Voltage	$\label{eq:linear} \begin{array}{l} 7.40V \leq V_{IN} \leq 25V, \\ 5 \text{ mA} \leq I_{OUT} \leq I_{MAX}, \\ P < P_{MAX} \end{array}$	4.6		5.4	4.75		5.25	V
Quiescent Current	$7.40V \leq V_{IN} \leq 25V$		5.2	10		5.2	10	mA
Quiescent Current Change	$\begin{array}{l} 7.40V \leq V_{IN} \leq 25V \\ 5 \text{ mA} \leq I_{OUT} \leq I_{MAX} \end{array}$			0.5 0.8			0.5 0.8	mA mA
Output Noise Voltage	$\begin{array}{l} T_{A} = 25^{\circ}C \\ 10 \; Hz \leq f \leq 100 \; kHz \end{array}$		40			40		μV
Long Term Stability			10			20		mV
Ripple Rejection	$T_j = 25^{\circ}C$	50			50			dB
Thermal Resistance, Junction to Case	(Note 2)							
TO-39 Package			15			15		°C/W
TO-3 Package			2.5			2.5		°C/W

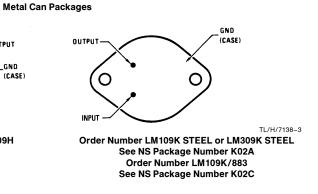
Note 1: Unless otherwise specified, these specifications apply $-55^{\circ}C \le T_j \le +150^{\circ}C$ for the LM109 and $0^{\circ}C \le T_j \le +125^{\circ}C$ for the LM309; $V_{IN} = 10V$; and $I_{OUT} = 0.1A$ for the TO-39 package or $I_{OUT} = 0.5A$ for the TO-3 package. For the TO-39 package, $I_{MAX} = 0.2A$ and $P_{MAX} = 2.0W$. For the TO-3 package, $I_{MAX} = 1.0A$ and $P_{MAX} = 20W$.

Note 2: Without a heat sink, the thermal resistance of the TO-39 package is about 150°C/W, while that of the TO-3 package is approximately 35°C/W. With a heat sink, the effective thermal resistance can only approach the values specified, depending on the efficiency of the sink. Note 3: Refer to RETS109H drawing for LM109H or RETS109K drawing for LM109K military specifications.

Connection Diagrams



Order Number LM109H, LM109H/883 or LM309H See NS Package Number H03A

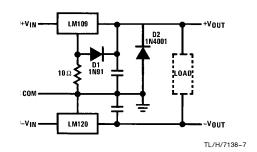


Application Hints

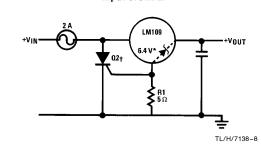
- a. Bypass the input of the LM109 to ground with \geq 0.2 μF ceramic or solid tantalum capacitor if main filter capacitor is more than 4 inches away.
- b. Avoid insertion of regulator into "live" socket if input voltage is greater than 10V. The output will rise to within 2V of the unregulated input if the ground pin does not make contact, possibly damaging the load. The LM109 may also be damaged if a large output capacitor is charged up, then discharged through the internal clamp zener when the ground pin makes contact.
- c. The output clamp zener is designed to absorb transients only. It will not clamp the output effectively if a failure occurs in the internal power transistor structure. Zener dynamic impedance is $\approx 4\Omega$. Continuous RMS current into the zener should not exceed 0.5A.
- d. Paralleling of LM109s for higher output current is not recommended. Current sharing will be almost nonexistent, leading to a current limit mode operation for devices with the highest initial output voltage. The current limit devices may also heat up to the thermal shutdown point ($\approx 175^{\circ}$ C). Long term reliability cannot be guaranteed under these conditions.

e. Preventing latchoff for loads connected to negative voltage:

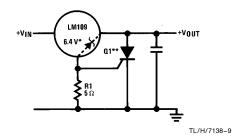
If the output of the LM109 is pulled negative by a high current supply so that the output pin is more than 0.5V negative with respect to the ground pin, the LM109 can latch off. This can be prevented by clamping the ground pin to the output pin with a germanium or Schottky diode as shown. A silicon diode (1N4001) at the output is also needed to keep the positive output from being pulled too far negative. The 10 Ω resistor will raise $+V_{OUT}$ by \approx 0.05V.





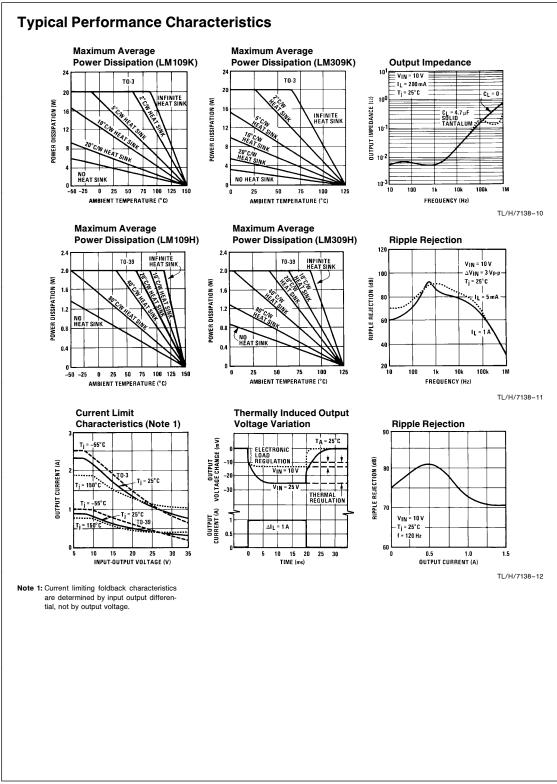




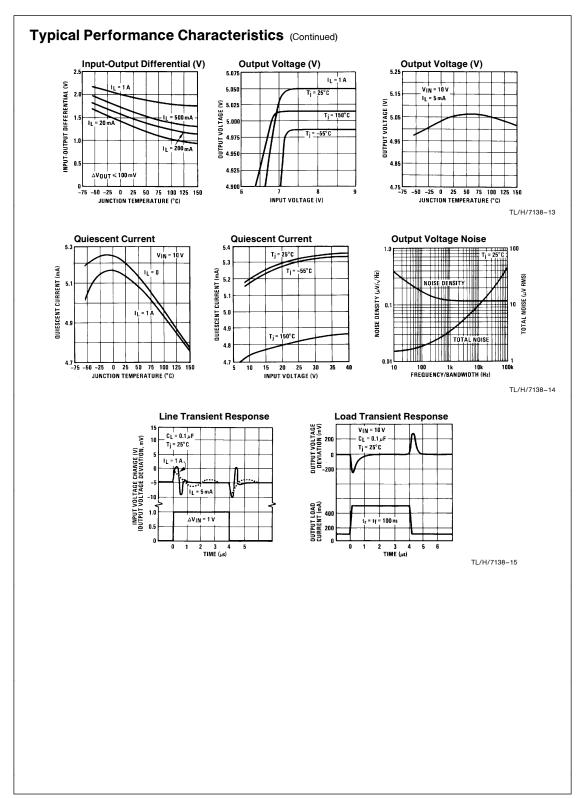


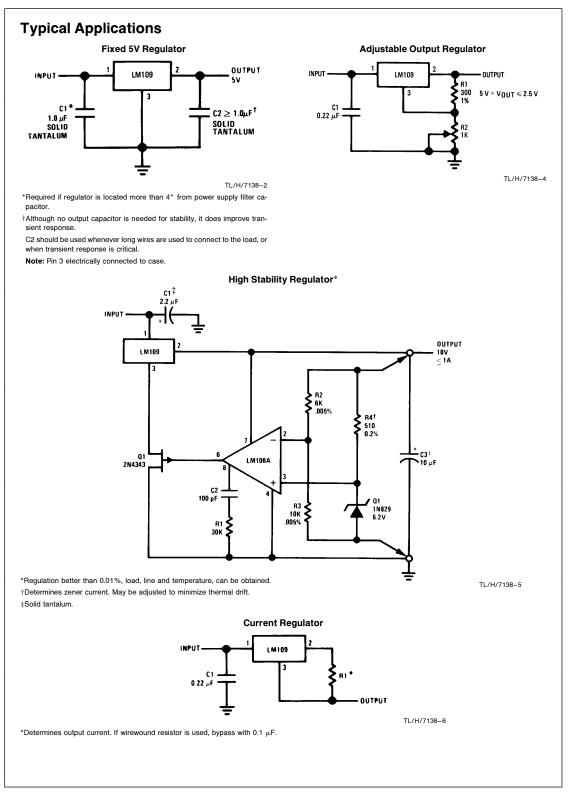
*Zener is internal to LM109.

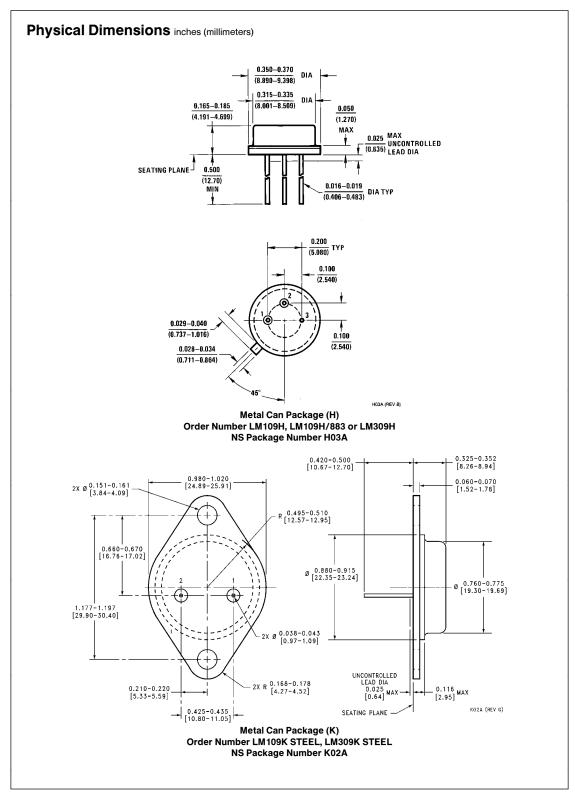
**Q1 must be able to withstand 7A continuous current if fusing is not used at regulator input. LM109 bond wires will fuse at currents above 7A. †Q2 is selected for surge capability. Consideration must be given to filter capacitor size, transformer impedance, and fuse blowing time. ††Trip point is ≈ 7.5V.

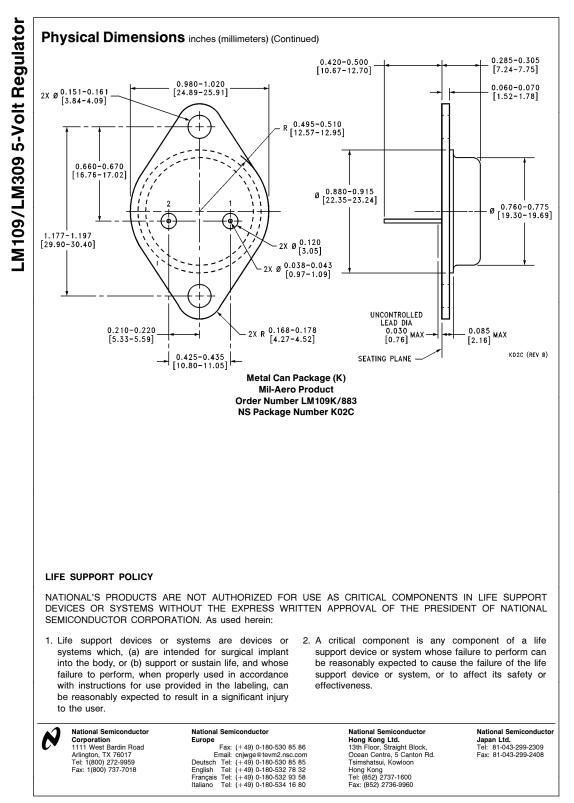












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