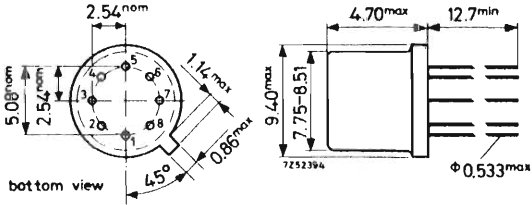


DIFFERENTIAL AMPLIFIER

The TAA201 is a silicon monolithic integrated differential amplifier using two Darlington connected pairs with a constant-current source for high input impedance, excellent input-output isolation and good temperature stability. The TAA201 can be used as a differential amplifier or as a single-ended input or output amplifier giving both inverting and non-inverting operation.

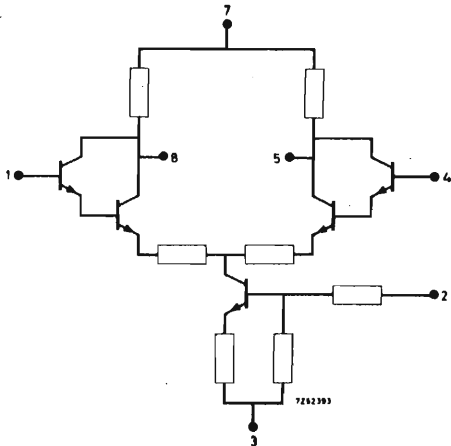


QUICK REFERENCE DATA

Ambient temperature	25 °C
Positive supply voltage	12 V
Negative supply voltage	6 V

Voltage gain	typ. 60
Common mode rejection	typ. 75 dB
Input offset voltage	typ. 7 mV
Input offset voltage drift	typ. 10 $\mu\text{V}/^\circ\text{C}$
Frequency response (-3 dB)	typ. 300 kHz
Input impedance	typ. 150 k Ω
Output impedance	typ. 8 k Ω
Output voltage range (peak-peak)	typ. 14.5 V
Package	A1 (TO-78)

CIRCUIT DIAGRAM



1. Input
2. Ground (supply return)
3. Negative supply
4. Input
5. Output
6. (not connected)
7. Positive supply
8. Output

7Z3 1635

CHARACTERISTICS

No load unless otherwise specified. $V_7 = 12\text{ V}$; $-V_3 = 6\text{ V}$.

		$T_{amb} (^\circ\text{C})$			
		-55	+25	+75	
Differential voltage gain	{ min. typ.		40 60	56	
Input offset voltage	{ typ. max.	4.0	7.0 10.0	7.0	mV mV
Input offset voltage change with temperature	{ typ. max.		10 20		$\mu\text{V}/^\circ\text{C}$ $\mu\text{V}/^\circ\text{C}$
Input bias current	{ typ. max.		0.3 1.2		μA μA
Input offset current	{ typ. max.	8.0	8.0 30	5.0	nA nA
Input offset current change with temperature	{ typ. max.		0.5 3.0		$\text{nA}/^\circ\text{C}$ $\text{nA}/^\circ\text{C}$
Common mode rejection ratio	{ min. typ. typ.		70 75 59		dB dB dB
			0 to 10 kHz 100 kHz		
Frequency response (-3 dB)	{ min. typ.		0 to 150 0 to 300		kHz kHz
Quiescent input voltage (V_1 ; V_4)	typ.	0	0	0	mV
Quiescent output voltage (V_8 ; V_5)	{ typ. max.		7.0 8.5		V V
Max. output voltage (peak-peak) at pin 8 and at pin 5	{ min. typ.		12.0 14.5		V V
Differential input impedance	{ min. typ.		75 150		$\text{k}\Omega$ $\text{k}\Omega$
Single-ended output impedance	{ typ. max.		8.0 10.0		$\text{k}\Omega$ $\text{k}\Omega$
Positive supply current (I_7)	typ.		0.9		mA
Negative supply current ($-I_3$)	typ.		2.6		mA
Power dissipation	{ typ. max.		26 33		mW mW

RATINGS (Limiting values) ¹⁾

Positive supply voltage (V_7)	max.	25	V
Negative supply voltage ($-V_3$)	max.	24	V
Power dissipation	max.	200	mW
Storage temperature (T_{stg})		-65 to +175	°C
Operating ambient temperature (T_{amb})		-55 to +75	°C

¹⁾ Limiting values according to the Absolute Maximum System as defined in IEC publication 134.